SYNOPSIS

Title:————————

submitted by

Mr./Ms.————————

In Partial Fulfillment of the Requirements for the Degree of

Doctor of Philosophy

under the guidance of

Dr.————————

Department of Electronics and Telecommunication Engineering

Bharatiya Vidya Bhavan's
Sardar Patel Institute of Technology
Munshi Nagar, Andheri West
Mumbai-58

April 2013

Faculty: Technology (Electronics and Telecommunication Engineering)

Area: ————————————————————-

Topic of Thesis: ————————————————-

Date of Admission: 17th January 2013

Name of Research Centre: Sardar Patel Institute of Technology, Andheri West, Mumbai-58

Name of Student: ————————————————

Name of Supervisor: ————————————————

Signature of student

Signature of Supervisor

Signature of H.O.D **Signature of Principal**

Dr. Y. S. Rao **Dr. Prachi Gharpure**

Motivation

Objectives

Literature Survey

Methodology

Outcomes of the Research

References

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