



**Sardar Patel Institute of Technology, Andheri, Mumbai
(Autonomous Institute Affiliated to University of Mumbai)
Department Of Electronics and Telecommunication Engineering**

New Facility Created

Module Discription	Any Other Contributory Inst/ Industry	Devel oped By	Duration of Develop ment	Resoures Consumed		Target Audience	Usage & Citation etc.
				Cost	Faculty Involved		
In CAY – 2017-18 (Antenna Design and Testing Lab Development)							
Ansysis Acadmic Research HF-1 Task Ansoft Desingner (RF & SI), HFSS, Slwave, Q3D Extracror, Optimetrics, ECAD Translators (Ansoft Links), MCAD Translators (Perpetual License)	S.P.I.T	S.P.I.T	6 month	430,000/-	Dr. Y. S. Rao Prof. R.S. Sonkusr e	Faculty and UG, PG & Phd. Student	Project Developme nt & R & D
In CAY – 2018-19 (PV Lab)							
3KW on grid solar inverter with RS485 Interface	S.P.I.T	S.P.I.T	6 month	51,920/-	Dr. Y. S. Rao Prof. Milind Paraye	Faculty and UG and PG Student	Project Developme nt & R & D
Microcontroller based based Inverter I and Vsensor with load	S.P.I.T	S.P.I.T	6 month	84,960/-	Dr. Y. S. Rao Prof. Milind Paraye	Faculty and UG and PG Student	Project Developme nt & R & D
Microcontroller based DC to DC converter	S.P.I.T	S.P.I.T	6 month	84,960/-	Dr. Y. S. Rao Prof. Milind Paraye	Faculty and UG and PG Student	Project Developme nt & R & D
NVIS 6005A I V Chara solar Cell	S.P.I.T	S.P.I.T	6 month	37,811/-	Dr. Y. S. Rao Prof. Milind Paraye	Faculty and UG and PG Student	Project Developme nt & R & D